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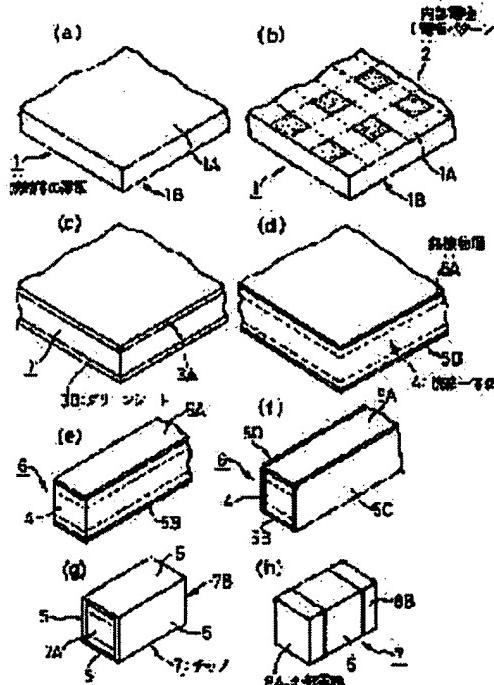
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## **(54) MULTILAYERED CHIP THERMISTOR AND MANUFACTURE THEREOF**



### **(57)Abstract:**

**PURPOSE:** To provide a multilayered chip thermistor having a high dimensional accuracy wherein inner electrodes are little dislocated by baking and dispersion of resistance values is little.

**CONSTITUTION:** External electrodes 8a and 8b are formed on both end faces of a thermistor base 7 made by sintering and unifying a multilayered body composed of a baked ceramic body 1 and ceramic green sheets 3A and 3B through electrodes thereon. Since the sintered body little reshrink at baking, the area of the electrodes formed on its surface, the distance between the opposed electrodes, and the distance of other electrodes formed on the same surface little change. The influence of the shrinkage of the green sheets on the resistance value is little and dispersion of the resistance values is avoidable. Since the sintered body little shrink but only the

green sheets at baking, the total shrinkage is low, dimensional dispersion is little and dimensional accuracy is high.

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